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Substitute for form 1449/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	
		Filing Date	
		First Named Inventor	Marinus W. Kruiskamp
		Art Unit	
		Examiner Name	
Sheet 1 of 2	Attorney Docket Number	P04769C2	

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
PN	AA	US- 5,596,289	01/21/1997	Liu	
	AB	US- 5,861,778	01/19/1999	Louagie, et al.	
	AC	US- 5,969,573	10/19/1999	Koike	
	AD	US- 6,002,299	12/14/1999	Thomsen	
	AE	US- 6,011,436	01/04/2000	Koike	
	AF	US- 6,111,437	08/29/2000	Patel	
PN	AG	US- 6,175,278	01/16/2001	Hasegawa	
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁵
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

Examiner Signature	Patricia Nguyen	Date Considered	2/7/06
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
PN	AH	H. Alzahrer and M. Ismail, "A CMOS Fully Balanced Differential Difference Amplifier and Its Application," IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 48, pp. 614-620, June 2001	
PN	AI	E. Lee, "Low-Voltage Opamp Design and Differential Difference Amplifier Design Using Linear Transconductor with Resistor Input," IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 47, pp 776-778, August 2000	

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